



HP0-661

(Non Stop Systems and Technologies)

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Question: 1

Which advantage does the NonStop S-series RISC processor provide over CISC processors in earlier systems?

- A. customized, special purpose instructions
- B. numerous addressing modes
- C. optimized instruction pipelines
- D. multicycle machine instructions

Answer: C

Question: 2

Which statement best describes disk cache blocks?

- A. Entry sequenced index blocks are always kept in cache.
- B. Different disk cache block sizes exist to assist in performance tuning.
- C. Different file types require different disk cache block sizes.
- D. Changes to disk cache block values take effect after the next system cold load.

Answer: B

Question: 3

Power to the NonStop S-series enclosures is set to the proper voltage by _____.

- A. configuring the power switch on the PMCU
- B. auto-ranging circuitry within the power supply
- C. ordering the proper part number for the voltage required
- D. ordering the power adapter appropriate to the voltage supplied

Answer: B

Question: 4

A service processor _____. Select TWO.

- A. has interfaces to both the ServerNet hardware and the serial maintenance bus (SMB)
- B. runs its own independent operating system
- C. connects directly to system fans
- D. routes Guardian interprocess communications
- E. manages communications only to the processor within its enclosure

Answer: A, B

Question: 5

What is a mandatory server?

- A. raised floor
- B. ventilated room
- C. air conditioning
- D. 240V power

top S74000

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Answer: B

Question: 6

Which term identifies

- A. adapter
- B. fabric
- C. SAC
- D. LIF

Answer: B

Question: 7

Which component does

- A. IOMF CRU
- B. SWAN concentrator
- C. E4SA
- D. PMF CRU

Controller?